

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Kalvin Williams
John Holcroft
Christopher Lane

Serial No. : 10/015,076
Filed : October 26, 2001
For : Hardware Semaphores For a Multi
Processor System Within A Shared
Memory Architecture

Group Art Unit : 2112
Examiner : Lee, Christopher E.
Atty Docket : 1496.00169 / 01-669

I hereby certify that this correspondence is being deposited with the U.S.
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Manu Kashyap

4/1/05

Date


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SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

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P. O. Box 1450
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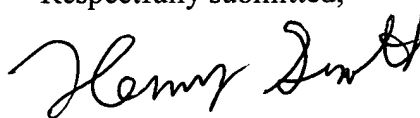
Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Date: 4/1/05

Respectfully submitted,



Henry Groth

Reg. No. 39,696